

EAST Search History**EAST Search History (Prior Art)**

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2	("5258325") or ("5731945").PN.	USPAT	OR	OFF	2009/09/12 22:15
L2	4060522	(integrat\$3 adj circuit\$1) or die\$1 or chip\$1 or semiconductor\$1	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/12 22:15
L3	1414149	(signal with condition\$3) or (amplif\$4 adj circuit\$1) or amplifier\$1 or (power adj amplifier\$1) or (low adj noise adj amplifier\$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/12 22:15
L4	1080052	"PA" or "LNA"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2009/09/12 22:15
L5	2392064	L3 or L4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/12 22:15
L6	3335	(ancillary with circuit\$1) or (voltage adj regulator\$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/12 22:15
L7	353092	(radio adj frequenc\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/12 22:15
L8	504438	"RF"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2009/09/12 22:15

L9	669917	L7 or L8	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2009/09/12 22:15
L10	377300	signal with path\$1	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/12 22:15
L11	841198	(substrate or wafer) with semiconductor\$1	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/12 22:15
L12	1233668	module\$1	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/12 22:15
L13	9	L2 and L5 and L6 and L9 and L10 and L11 and L12	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/12 22:15
L14	25	L2 and L5 and L6 and L9 and L10 and L12	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/12 22:15
L15	73971	first adj ((integrat\$3 adj circuit\$1) or die\$1 or chip \$1 or semiconductor\$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/12 22:15
L16	68775	second adj ((integrat\$3 adj circuit\$1) or die\$1 or chip\$1 or semiconductor \$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/12 22:15
L17	54432	L15 and L16	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/12 22:15

L18	841198	(substrate or wafer) with semiconductor\$1	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/12 22:15
L19	1414149	(signal with condition\$3) or (amplif\$4 adj circuit\$1) or amplifier\$1 or (power adj amplifier\$1) or (low adj noise adj amplifier\$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/12 22:15
L20	1080052	"PA" or "LNA"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2009/09/12 22:15
L21	2392064	L19 or L20	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/12 22:15
L22	353092	(radio adj frequenc\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/12 22:15
L23	504438	"RF"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2009/09/12 22:15
L24	669917	L22 or L23	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/12 22:15
L25	2520	L17 and L21 and L24	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/12 22:15
L26	3335	(ancillary with circuit\$1) or (voltage adj regulator\$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/12 22:15

L27	7	L25 and L26	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/12 22:15
L28	0	("2007/0259639").URPN.	USPAT	OR	OFF	2009/09/12 22:15
L29	1	2005-322513.NRAN.	DERWENT	OR	OFF	2009/09/12 22:15
L30	1	("6806771").URPN.	USPAT	OR	OFF	2009/09/12 22:15
L31	34	("3530395" "4250556" "4855685" "5068823" "5107146" "5196740" "5285169" "5311142" "5363055" "5416484" "5463349" "5510738" "5528179" "5554957" "5557234" "5574678" "5631606" "5680070" "5821776" "5877536" "5877612" "5912567" "5912583" "5952867" "5959871" "5966047" "5994926" "6014097" "6043709" "6111437" "6118266" "6140867" "6201429").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2009/09/12 22:15
L32	68893	ancillary or (voltage adj regulator\$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/12 22:15
L33	24	L25 and L32	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/12 22:15
L34	65268	(ancillary with circuit\$1) or (voltage adj regulator\$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/12 22:15
L35	98	L25 and L34	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/12 22:15

L36	14194	"BiCMOS"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2009/09/12 22:15
L37	15	L35 and L36	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/12 22:15
L38	3	"7302247"	USPAT	OR	OFF	2009/09/12 22:15
L39	2	"7185244"	USPAT	OR	OFF	2009/09/12 22:15
L40	55709	(ancillary with circuit\$1) or ((voltage adj regulator\$1) and circuit\$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/12 22:15
L41	9601	(ancillary with circuit\$1) or ((voltage adj regulator\$1) adj circuit\$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/12 22:15
L42	73971	first adj ((integrat\$3 adj circuit\$1) or die\$1 or chip \$1 or semiconductor\$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/12 22:15
L43	68775	second adj ((integrat\$3 adj circuit\$1) or die\$1 or chip\$1 or semiconductor \$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/12 22:15
L44	54432	L42 and L43	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/12 22:15
L45	130	L41 and L44	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/12 22:15

L46	14194	"BiCMOS"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2009/09/12 22:15
L47	4	L45 and L46	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/12 22:15
L48	90	("3713148" "3714540" "3859624" "4523128" "4536715" "4650981" "4675579" "4763075" "4785345" "4868647" "4924210" "4931867" "4937468" "4959631" "5095357" "5128729" "5168863" "5369666" "5384808" "5404545" "5442303" "5444740" "5448469" "5469098" "5555421" "5572179" "5591996" "5615091" "5615229" "5625265" "5650357" "5654984" "5663672" "5716323" "5774791" "5781071" "5781077" "5786763" "5789960" "5801602" "5812597" "5812598" "5845190" "5850436" "5864607" "5900683" "5907481" "5913817" "5926358" "5945728" "5952849" "5969590" "6049258" "6061009" "6069802" "6087882" "6114937" "6124756" "6137372" "6222922" "6232902" "6249171" "6262600" "6291907" "6307497" "6384763" "6389063" "6452519" "6525566" "6603807" "6611051" "6670861" "6728320" "6747522" "6833800" "6873065" "6903578" "6914547" "6922080" "6927662" "6940445" "6956727" "6967513" "6977522" "7016490" "7023372" "7053807" "7057491" "7075329" "7102388").	OR	OFF	2009/09/12 22:15	

		PN.				
L49	41	battery near2 (substitute or (back\$1up or backup)) and (regulator near4 voltage) same (second or additional or two) and ((regulator near4 voltage) same (second or additional or two) same chip)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/12 22:15
L50	10	("5844853").URPN.	USPAT	OR	OFF	2009/09/12 22:15
L51	3	(("5144585") or ("4994688") or ("5297097")).PN.	USPAT	OR	OFF	2009/09/12 22:15
L52	1	("5485509").PN.	USPAT	OR	OFF	2009/09/12 22:15
L53	3	(("5144585") or ("4994688") or ("5297097")).PN.	USPAT	OR	OFF	2009/09/12 22:15
L54	1	("5485509").PN.	USPAT	OR	OFF	2009/09/12 22:15
L55	0	mask\$1slice adj technique	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/12 22:15
L56	15	master\$1slice adj technique	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/12 22:15
L57	733	"256" adj Mbit adj "DRAM"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/12 22:15
L58	1180767	chip\$1	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/12 22:15
L59	381	L57 and L58	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/12 22:15

L60	8	("3.3V" adj DRAM)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/12 22:15
L61	2	("2.5V" adj DRAM)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/12 22:15
L62	8	L60 and L60	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/12 22:15
L63	0	L59 and L62	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/12 22:15
L64	0	L60 and L61	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/12 22:15
L65	155	("3.3V" with DRAM)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/12 22:15
L66	186	("2.5V" with DRAM)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/12 22:15
L67	48	L65 and L66	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/12 22:15
L68	0	L67 and L59	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/12 22:15

L69	0	L60 and L57	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/12 22:15
L70	11	"3.3V" adj ("DRAM" or (dynamic adj random adj access adj memory))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/12 22:15
L71	9	"3.3V" adj ("DRAM" or (dynamic adj random adj access adj memory))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/12 22:15
L72	46058	L15 same L16	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/12 22:20
L73	9	(first adj ancillary adj circuit) same (second ancillary adj circuit)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/12 22:24
L74	5	72 and 73	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/12 22:24
L75	1	("6734553").PN.	USPAT	OR	OFF	2009/09/12 22:29
L76	0	(first adj ancillary adj circuit) same (first adj chip)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/12 22:58
L77	1	(first adj ancillary adj circuit) same (first adj ic)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/12 22:58

L78	0	(first adj ancillary adj circuit) same chip	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/12 22:59
L79	0	(second adj ancillary adj circuit) same chip	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/12 22:59
L80	60	(ancillary adj circuit) same chip	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/12 22:59
L81	9	(ancillary adj circuit) with integrat\$3 with chip	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/12 23:03
L82	15	(ancillary adj circuit) same integrat\$3 with chip	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/12 23:05
L83	6	82 not 81	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/12 23:05
L84	16	(ancillary adj circuit) with integrat\$3 with (chip or die)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/12 23:07
L85	1	2008-A73628.NRAN.	DERWENT	OR	OFF	2009/09/12 23:09
L86	1	2008-A73628.NRAN.	DERWENT	OR	OFF	2009/09/12 23:11
L87	16	(ancillary adj circuit) with integrat\$3 same (chip or die)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/12 23:12

L88	11	("20010011926" "20030165052" "5258325" "5731945" "5844853" "5983089" "6054900" "6075995" "6734553" "7164905" "7245884").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2009/09/12 23:14
L89	0	("7409200").URPN.	USPAT	OR	OFF	2009/09/12 23:16
S1	2	(("5258325") or ("5731945")).PN.	USPAT	OR	OFF	2007/11/29 15:46
S2	3503382	(integrat\$3 adj circuit\$1) or die\$1 or chip\$1 or semiconductor\$1	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/04 22:16
S3	1276900	(signal with condition\$3) or (amplif\$4 adj circuit\$1) or amplifier\$1 or (power adj amplifier\$1) or (low adj noise adj amplifier\$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/11/29 15:56
S4	986463	"PA" or "LNA"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/11/29 15:57
S5	2173816	S3 or S4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/04 22:19
S6	3103	(ancillary with circuit\$1) or (voltage adj regulator\$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/04 22:32
S7	268660	(radio adj frequenc\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/11/29 16:01
S8	418073	"RF"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/11/29 16:01

S9	544266	S7 or S8	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/12/04 22:20
S10	331486	signal with path\$1	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/11/29 16:04
S11	723259	(substrate or wafer) with semiconductor\$1	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/04 22:18
S12	931688	module\$1	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/11/29 16:05
S13	6	S2 and S5 and S6 and S9 and S10 and S11 and S12	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/11/29 16:18
S14	20	S2 and S5 and S6 and S9 and S10 and S12	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/11/29 16:19
S15	62602	first adj ((integrat\$3 adj circuit\$1) or die\$1 or chip \$1 or semiconductor\$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/04 22:17
S16	58344	second adj ((integrat\$3 adj circuit\$1) or die\$1 or chip\$1 or semiconductor \$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/04 22:17
S17	45686	S15 and S16	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/05 19:53

S18	723675	(substrate or wafer) with semiconductor\$1	US_PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/04 22:18
S19	1277433	(signal with condition\$3) or (amplif\$4 adj circuit\$1) or amplifier\$1 or (power adj amplifier\$1) or (low adj noise adj amplifier\$1)	US_PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/04 22:19
S20	986776	"PA" or "LNA"	US_PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/12/04 22:19
S21	2174625	S19 or S20	US_PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/04 22:19
S22	268930	(radio adj frequenc\$3)	US_PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/04 22:20
S23	418359	"RF"	US_PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/12/04 22:20
S24	544674	S22 or S23	US_PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/04 22:20
S25	1947	S17 and S21 and S24	US_PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/04 22:25
S26	3103	(ancillary with circuit\$1) or (voltage adj regulator\$1)	US_PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/05 19:51

S27	5	S25 and S26	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/04 22:52
S28	0	("2007/0259639").URPN.	USPAT	OR	OFF	2007/12/04 22:33
S29	1	2005-322513.NRAN.	DERWENT	OR	OFF	2007/12/04 22:36
S30	0	("6806771").URPN.	USPAT	OR	OFF	2007/12/04 22:37
S31	34	("3530395" "4250556" "4855685" "5068823" "5107146" "5196740" "5285169" "5311142" "5363055" "5416484" "5463349" "5510738" "5528179" "5554957" "5557234" "5574678" "5631606" "5680070" "5821776" "5877536" "5877612" "5912567" "5912583" "5952867" "5959871" "5966047" "5994926" "6014097" "6043709" "6111437" "6118266" "6140867" "6201429").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2007/12/04 22:37
S32	61082	ancillary or (voltage adj regulator\$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/04 22:42
S33	14	S25 and S32	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/04 22:42
S34	57157	(ancillary with circuit\$1) or (voltage adj regulator\$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/04 22:51
S35	58	S25 and S34	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/04 23:29

S36	12173	"BiCMOS"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/12/05 20:02
S37	13	S35 and S36	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/04 23:29
S38	2	"7302247"	USPAT	OR	OFF	2007/12/05 15:09
S39	1	"7185244"	USPAT	OR	OFF	2007/12/05 18:59
S40	48562	(ancillary with circuit\$1) or ((voltage adj regulator\$1) and circuit\$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/05 19:52
S41	8480	(ancillary with circuit\$1) or ((voltage adj regulator\$1) adj circuit\$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/05 19:52
S42	62602	first adj ((integrat\$3 adj circuit\$1) or die\$1 or chip \$1 or semiconductor\$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/05 19:53
S43	58344	second adj ((integrat\$3 adj circuit\$1) or die\$1 or chip\$1 or semiconductor \$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/05 19:53
S44	45686	S42 and S43	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/05 20:01
S45	101	S41 and S44	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/05 20:03

S46	12173	"BiCMOS"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/12/05 20:02
S47	3	S45 and S46	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/05 20:03
S48	90	("3713148" "3714540" "3859624" "4523128" "4536715" "4650981" "4675579" "4763075" "4785345" "4868647" "4924210" "4931867" "4937468" "4959631" "5095357" "5128729" "5168863" "5369666" "5384808" "5404545" "5442303" "5444740" "5448469" "5469098" "5555421" "5572179" "5591996" "5615091" "5615229" "5625265" "5650357" "5654984" "5663672" "5716323" "5774791" "5781071" "5781077" "5786763" "5789960" "5801602" "5812597" "5812598" "5845190" "5850436" "5864607" "5900683" "5907481" "5913817" "5926358" "5945728" "5952849" "5969590" "6049258" "6061009" "6069802" "6087882" "6114937" "6124756" "6137372" "6222922" "6232902" "6249171" "6262600" "6291907" "6307497" "6384763" "6389063" "6452519" "6525566" "6603807" "6611051" "6670861" "6728320" "6747522" "6833800" "6873065" "6903578" "6914547" "6922080" "6927662" "6940445" "6956727" "6967513" "6977522" "7016490" "7023372" "7053807" "7057491" "7075329" "7102388").	OR	OFF	2007/12/05 23:52	

		PN.				
S49	35	battery near2 (substitute or (back\$1up or backup)) and (regulator near4 voltage) same (second or additional or two) and ((regulator near4 voltage) same (second or additional or two) same chip)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/18 20:02
S50	8	("5844853").URPN.	USPAT	OR	OFF	2007/12/18 20:28
S51	3	(("5144585") or ("4994688") or ("5297097")).PN.	USPAT	OR	OFF	2007/12/18 22:05
S52	1	("5485509").PN.	USPAT	OR	OFF	2007/12/18 22:05
S53	3	(("5144585") or ("4994688") or ("5297097")).PN.	USPAT	OR	OFF	2008/10/22 13:48
S54	1	("5485509").PN.	USPAT	OR	OFF	2008/10/22 13:49
S55	0	mask\$1slice adj technique	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/22 13:51
S56	15	master\$1slice adj technique	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/22 13:52
S57	700	"256" adj Mbit adj "DRAM"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/22 13:55
S58	1075230	chip\$1	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/22 13:55
S59	359	S57 and S58	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/22 13:55

S60	8	("3.3V" adj DRAM)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/22 14:17
S61	1	("2.5V" adj DRAM)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/22 14:18
S62	8	S60 and S60	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/22 14:18
S63	0	S59 and S62	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/22 14:18
S64	0	S60 and S61	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/22 14:18
S65	149	("3.3V" with DRAM)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/22 14:22
S66	175	("2.5V" with DRAM)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/22 14:22
S67	46	S65 and S66	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/22 14:22
S68	0	S67 and S59	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/22 14:22

S69	0	S60 and S57	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/22 14:40
S70	10	"3.3V" adj ("DRAM" or (dynamic adj random adj access adj memory))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/22 14:45
S71	9	"3.3V" adj ("DRAM" or (dynamic adj random adj access adj memory))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/22 14:46

9/12/09 11:20:00 PM

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